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- S1: (1435) (330/10).CCLS.
- S5: (986) (330/151).CCLS.
- S7: (622) (330/136).CCLS.
- S9: (1900) (330/124R).CCLS.
- S10: (282) (330/207A).CCLS.
- S11: (1208) (330/51).CCLS.
- S12: (373) (330/251).CCLS.
- S13: (866) (330/53).CCLS.
- S14: (832) (330/295).CCLS.
- S61: (15) (amplifier nearl polar) and (mode or select\$3)
- S62: (44) (amplifier nearl polar) and (mode or select\$3)
- S73: (18) amplifier and ((mode nearl select\$3) nearl
- S74: (169) amplifier and ((mode nearl select\$3) nearl
- S83: (454) (amplifier same plurality) and ((mode or se

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Default operator OR highlight all hit terms initially

(amplifier nearl polar) and (mode or select\$3)

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	U	E	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050110568 A	20050526	15	Modified polar amplifier architecture	330/151	330/149		Robinson, Ian et al.
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050110565 A	20050526	16	Multiple polar amplifier architecture	330/124R			Robinson, Ian
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050017802 A	20050127	18	Digital cross cancellation system	330/151	330/149		Robinson, Ian
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040266366 A	20041230	19	Multi-mode amplifier system	455/91	455/127.1		Robinson, Ian et al.
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040263246 A	20041230	20	MULTI-MODE MULTI-AMPLIFIER ARCHITECTURE	330/124R			Robinson, Ian et al.
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040263245 A	20041230	17	Polar and linear amplifier system	330/10			Winter, Frank et al.
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040208157 A	20041021	28	Multi-mode communications transmitter	370/345	370/336; 370/337;		Sander, Brian et al.
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040198257 A	20041007	31	Communication semiconductor integrated circuit, a wire	455/108	455/127.1		Takano, Ryoichi et al.
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040192369 A	20040930	7	Method and apparatus for reducing dynamic range of a	455/522	455/127.1		Nilsson, Magnus
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040164796 A	20040826	34	Low-noise active rc signal processing circuit	330/75			Nakamura, Masataka et al.

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